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2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Applicant: Jack Hwang et al.

Serial No.: 10/644,417

Filed: August 20, 2003

For: Forming Strained Source Drain
Junction Field Effect Transistors

§
§ Art Unit: 2822
§
§
§ Examiner: Mark V. Prenty
§
§
§ Docket: ITL.0764D1US
§ P14416D
§

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

OK
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REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed August 4, 2004, please amend the above-referenced patent application as follows:

Date of Deposit: August 10, 2004

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Cynthia L. Hayden